

### R E M A R K S

Careful review and examination of the subject application are noted and appreciated. Applicants' representative thanks Examiner Patel for the indication of allowable matter.

### CLAIM OBJECTIONS

The objection to claims 9 and 20 has been obviated by appropriate amendment and should be withdrawn.

### SUPPORT FOR CLAIM AMENDMENTS

Support for amended claims 1, 19 and 20 can be found in the drawings as originally filed (for example, in FIG. 2), and on page 5, lines 5 through 7 of the specification as originally filed. As such, no new matter has been added. Applicants' representative asserts that the amendments should not be considered new issues since the Examiner failed to give the term "multiphased" a reasonable interpretation in the last Office Action.

### CLAIM REJECTIONS UNDER 35 U.S.C. §102

The rejection of claims 1-14 and 17-20 under 35 U.S.C. §102 as being anticipated by Kuo is respectfully traversed and should be withdrawn.

The rejection of claims 1-10 and 17-20 under 35 U.S.C. §102 as being anticipated by Chaudhry is respectfully traversed and should be withdrawn.

Kuo discloses a digital-edge-rate control LVDS driver (Title). Chaudhry discloses a skewless differential switching scheme for current-mode digital-to-analog converters (Title).

In contrast, claim 1 of the present invention provides an apparatus comprising a first plurality of parallel switches and a second plurality of parallel switches. The first plurality of parallel switches may be configured to (i) each receive a multiphased data signal having N phases, where N is a positive integer and (ii) control a first voltage on a first output pin. The second plurality of parallel switches may be configured to (i) each receive a digital complement of the multiphased data signal and (ii) control a second voltage on a second output pin. The first and second pluralities of parallel switches may be configured to provide rise time control of a differential waveform. Claims 19 and 20 provide similar limitations. Neither Kuo nor Chaudhry disclose or suggest such a configuration.

In particular, the Examiner points to the transistors MA and MB in FIGS. 3 and 5 of Kuo as the claimed first plurality of switches. However, the devices MA and MB of Kuo cannot be the claimed first plurality of switches since the devices MA and MB each **receive different signals**. In particular, the switch MA of Kuo receives a signal A and the switch MB of Kuo receives a signal B. Clearly, the signal A and the signal B of Kuo are separate signals (see column 7, lines 23-25 of Kuo). The statement that the signal NA is a compliment of the signal A and the signal NB is a compliment of the signal B (both of which are true) does not cure

the fact that the signals A and B are **different signals**. In the claimed invention, the first plurality of switches are configured to **each receive the** (same) multiphase data signal (e.g., see the signal DataN in FIG. 2).

Furthermore, the switch MNA of Kuo receives the signal NA and the switch MNB receives the signal NB. Kuo specifically discloses that the signal NA and the signal NB correspond to separate signals (see column 7, lines 23-25 of Kuo). In contrast, the claimed second plurality of switches are **each** configured to receive a digital complement of **the** multiphased data signal (e.g., see the signal DataN in FIG. 2). Additionally, the signal A, the signal NA, the signal B and the signal NB of Kuo are not multiphased signals, as presently claimed. Kuo is silent regarding multiphased signals. Applicants' representative has performed a search in Kuo for the word "multiphase". No references to "multiphase" were found in Kuo. Therefore, Kuo does not disclose each and every element of the claimed invention. As such, the presently claimed invention is fully patentable over Kuo and the rejection should be withdrawn.

Similarly, Chaudhry also does not disclose or suggest the each and every element of the presently claimed invention. The assertion that the transistors shown in FIG. 2 of Chaudhry receive a multiphased data signal is not correct. Nothing in Chaudhry describes the signal D or DB as a multiphased signal. A search was performed in Chaudhry for the word "multiphased". No

references were found. Additionally, the Office Action asserts that the claimed multiphased data is somehow the same as "high" and "low" (see page 2, line 8 of the Office Action). Applicants' representative disagrees. One skilled in the art would understand that a high or low portion of a data signal corresponds to either the state or the polarity of the data signal, but not the phase. Chaudhry is silent regarding a multiphased data signal. It is unclear whether any of the signals in FIG. 2 of Chaudhry are multiphased signals. Therefore, Chaudhry does not disclose or suggest each and every element of the claimed invention. As such, the presently claimed invention is fully patentable over the cited references and the rejection should be withdrawn.

**CLAIM REJECTIONS UNDER 35 U.S.C. §103**

The rejection of claims 11-14 under 35 U.S.C. §103 as being unpatentable over Chaudhry in view of Kenney is respectfully traversed and should be withdrawn. Claims 11-14 depend, indirectly, on claim 1, which is now believed to be allowable.

As such, the presently claimed invention is fully patentable over the cited references and the rejection should be withdrawn.

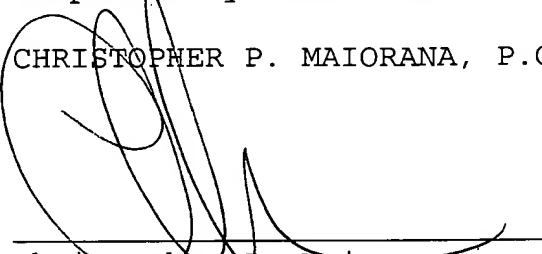
Accordingly, the present application is in condition for allowance. Early and favorable action by the Examiner is respectfully solicited.

The Examiner is respectfully invited to call the Applicants' representative at 586-498-0670 should it be deemed beneficial to further advance prosecution of the application.

If any additional fees are due, please charge Deposit Account No. 12-2252.

Respectfully submitted,

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